

Course structure

B. Tech. Second year (Electronics & Communication Engineering)

Year II, Semester III

| S. No. | Course Code | NAME OF THE SUBJECT | PERIODS | | | Credit |
|--------|-------------|-----------------------------------|---------|---|----|--------|
| | | | L | T | P | |
| 1 | EAS-301 | Engg. Mathematics-III | 3 | 1 | 0 | 4 |
| 2 | EEC-301 | Network Analysis & Synthesis | 3 | 1 | 0 | 4 |
| 3 | EEC-302 | Fundamental of Electronic Devices | 3 | 1 | 0 | 4 |
| 4 | EEC-303 | Signals and Systems | 3 | 1 | 0 | 4 |
| 5 | EHU-301 | Industrial Sociology | 2 | 0 | 0 | 2 |
| 6 | EEC-304 | Switching Theory & Logic Design | 2 | 1 | 0 | 3 |
| 7 | AUC-001 | <i>Cyber Security</i> | 2 | 0 | 0 | |
| 8 | EEC-351 | Network Analysis & Synthesis Lab. | 0 | 0 | 3 | 1 |
| 9 | EEC-352 | Electronics Workshop & PCB Design | 0 | 0 | 3 | 1 |
| 10 | EEC-353 | Logic Design Lab. | 0 | 0 | 2 | 1 |
| 11 | EEC-354 | Electronic Device Lab. | 0 | 0 | 2 | 1 |
| | GP-301 | GP | | | | -- |
| | | Total | 18 | 5 | 10 | 25 |

Semester IV

| S. No. | Course Code | NAME OF THE SUBJECT | PERIODS | | | Credit |
|--------|---------------------|---|---------|---|----|--------|
| | | | L | T | P | |
| 1 | EOE-031- EOE-038 | Science based Elective* | 3 | 1 | 0 | 4 |
| 2 | EEC-401 | Data Structure | 3 | 1 | 0 | 4 |
| 3 | EEC-402 | Electronic Circuits | 3 | 1 | 0 | 4 |
| 4 | EEC-403 | Electronic Measurements & Instrumentation | 3 | 1 | 0 | 4 |
| 5 | EHU-401 | Industrial Psychology | 2 | 0 | 0 | 2 |
| 6 | EEC-404 | Electromagnetic Field Theory (EMFT) | 2 | 1 | 0 | 3 |
| 7 | AUC-002 | <i>Human Values & Professional Ethics</i> | 2 | 0 | 0 | - |
| 8 | EEC-451 | Data Structure Lab. | 0 | 0 | 3 | 1 |
| 9 | EEC-452 | Electronic Circuits Lab. | 0 | 0 | 3 | 1 |
| 10 | EEC-453 | Signals and Systems lab | 0 | 0 | 2 | 1 |
| 11 | EEC-454 | Electronics Measurement Lab. | 0 | 0 | 2 | 1 |
| | GP-401 | GP | | | | -- |
| | | Total | 18 | 5 | 10 | 25 |

*SCIENCE BASED OPEN ELECTIVE

| | |
|---------|---|
| EOE-041 | Introduction to Soft Computing (Neural Networks, Fuzzy Logic and Genetic Algorithm) |
| EOE-042 | Nano Sciences |
| EOE-043 | Laser Systems and Applications |
| EOE-044 | Space Sciences |
| EOE-045 | Polymer Science & Technology |
| EOE-046 | Nuclear Science |
| EOE-047 | Material Science |

**Cyber Security will be offered as a compulsory audit course as directed by the UGC.

Semester III

EAS-301: ENGINEERING MATHEMATICS–III

Unit-I: Function of Complex variable

8

Analytic function, C-R equations, Harmonic Functions, Cauchy's integral theorem, Cauchy's integral formula, Derivatives of analytic functions, Taylor's and Laurent's series, Singularities, Zeroes and Poles, Residue theorem, Evaluation of real integrals of the type .

$$\int_0^{2\pi} f(\cos\theta, \sin\theta)d\theta \text{ and } \int_{-\infty}^{\infty} f(x)dx.$$

Unit–II: Integral Transforms

8

Fourier integral, Complex Fourier transform, Inverse Transforms, Convolution Theorems, Fourier sine and cosine transform, Applications of Fourier transform to simple one dimensional heat transfer equations, wave equations and Laplace equations. Z-transform and its application to solve difference equations

Unit–III:Statistical Techniques - Moments, Moment generating functions, **Central Tendency, depression** Skewness, Kurtosis, Curve fitting, Method of leastsquares, Fitting of straight lines, Polynomials, Exponential curves, Correlation, Linear, non-linear and multiple regression analysis, **Problems, Bayes Theorem, random Variables** Binomial, Poisson and Normal distributions, Tests of significations: Chi-square test, t-test

Unit–IV: Numerical Techniques–I

8

Zeroes of transcendental and polynomial equations using Bisection method, Regula-falsi method and Newton-Raphson method, Rate of convergence of above methods. Interpolation: Finite differences, Newton's forward and backward interpolation, Lagrange's and Newton's divided difference formula for unequal intervals.

Unit–V: Numerical Techniques–II

8

Solution of system of linear equations, Matrix Decomposition methods, Jacobi method, Gauss-Seidal method. Numerical differentiation, Numerical integration, Trapezoidal rule, Simpson's one third and three-eighth rules, Solution of ordinary differential equations (first order, second order and simultaneous) by Euler's, Picard's and fourth-order Runge-Kutta methods.

Test Books:-

1. Peter V. O'Neil, Advance Engineering Mathematics Thomson (Cengage) Learning, 2007.
2. Jain, Iyenger Jain, Numerical Methods for Scientific and Engineering Computation, New Age International, New Delhi
3. J.N. Kapur, Mathematical Statistics, S. Chand & company Ltd.
4. B.S.Grewal, Higher Engineering Mathematics, Khanna Publishers

Reference Books:-

1. R.K. Jain & S.R.K. Iyenger, Advance Engineering Mathematics, Narosa Publication House,.
2. Chandrika Prasad, Advanced Mathematics for Engineers, Prasad Mudralaya, 1996.
3. S.S.Sastry, Introductory Methods of Numerical Analysis, PHI Learning Private Limited, New Delhi
4. E. Balagurusamy, Numerical Methods, Tata McGraw-Hill Publishing Company Limited, New Delhi
5. T. Veerajan & T. Ramchandrandran, Theory & Problems in Numerical Methods, TMH, New Delhi

EEC-301 NETWORK ANALYSIS & SYNTHESIS

UNIT-1

Signal analysis, complex frequency, network analysis, network synthesis, General characteristics and descriptions of signals, step function and associated wave forms, The unit impulse Introduction to network analysis, network elements, initial and final conditions, step and impulse response, solution of network equations,

UNIT-2

Review of Laplace transform, poles and zeroes, initial and final value theorems, The transform circuit, Thevenin's and Norton's theorems, the system function, step and impulse responses, the convolution integral. Amplitude and phase responses. Network functions, relation between port parameters, transfer functions using two port parameters, interconnection of two ports.

UNIT-3

Hurwitz polynomials, positive real functions. Properties of real immittance functions, synthesis of LC driving point immittances, properties of RC driving point impedances, synthesis of RC impedances or RL admittances, properties of RL impedances and RC admittances..

UNIT-4

Properties of transfer functions, zeroes of transmission, synthesis of Y_{21} and Z_{21} with 1Ω terminations.

UNIT-5

Introduction to active network synthesis, Active Network Synthesis.

Text Book:

1. Franklin F. Kuo, "Network Analysis and synthesis", 2nd Edition, Wiley India Pvt Ltd.
2. Behrouz Peikari, "Fundamentals of Network Analysis & synthesis", Jaico Publishing House, 2006.

Reference Books: M. E. Van Valkenberg, "Network Analysis", 2nd Edition, Prentice Hall of India Ltd.

Ghosh-Network Theory: Analysis and Synthesis, PHI Learning Pvt. Ltd

EEC-302 FUNDAMENTAL OF ELECTRONIC DEVICES

UNIT-1

Crystal Properties and charge Carriers in Semiconductors: Elemental and compound semiconductor materials, crystal lattice structure Bonding forces and energy bands in solids, charge carriers in semiconductors, carrier concentrations, drift of carriers in electric and magnetic fields.

UNIT-2

Junction Properties: Equilibrium conditions, biased junctions, steady state conditions, reverse bias break down, transient and AC conditions. Metal semiconductor junctions.

UNIT-3

Transistors: Metal-semiconductor-field-effect-transistors (MESFET), Metal-insulator-semiconductor-field-effect-transistors (MISFET), Metal oxide semiconductor field effect transistor (MOSFET): Construction, Operation and characteristics of above devices. Bipolar junction transistors: Fundamentals of BJT operation, amplification with BJTs

UNIT-4

Some special devices:

Photodiodes, photo detectors, solar cell, light emitting diodes, light emitting materials.

Tunnel Diode: degenerate semiconductors; The transferred electron mechanism: The GUNN diode. P-N-P-N diode, semiconductor controlled rectifier (SCR).

UNIT-5

Excess Carriers in Semiconductors: Optical absorption, luminescence, carrier life time and photo conductivity, diffusion of carriers

Text Book:

B.G Streetman and S.Banerjee “Solid State electronics devices”, 5th Edition, PHI.

Reference Books: Donald a Neaman, “Semiconductor Physics and Devices Basic Principles”, 3rd Ed TMH India.

EEC-303 SIGNALS AND SYSTEMS

UNIT-1

Signals: Definition, types of signals and their representations: continuous-time/discrete-time, periodic/non-periodic, even/odd, energy/power, deterministic/ random, one-dimensional/multidimensional; commonly used signals (in continuous-time as well as in discrete-time): unit impulse, unit step, unit ramp (and their interrelationships), exponential, rectangular pulse, sinusoidal; operations on continuous-time and discrete-time signals (including transformations of independent variables).

UNIT-2

Laplace-Transform (LT) and Z-transform (ZT):

- (i) One-sided LT of some common signals, important theorems and properties of LT, inverse LT, solutions of differential equations using LT, Bilateral LT, Regions of convergence (ROC)
- (ii) One sided and Bilateral Z-transforms, ZT of some common signals, ROC, Properties and theorems, solution of difference equations using one-sided ZT, s- to z-plane mapping

UNIT-3

Fourier Transforms (FT):

- (i) Definition, conditions of existence of FT, properties, magnitude and phase spectra, Some important FT theorems, Parseval's theorem, Inverse FT, relation between LT and FT
- (ii) Discrete time Fourier transform (DTFT), inverse DTFT, convergence, properties and theorems, Comparison between continuous time FT and DTFT

UNIT-4

Systems: Classification, linearity, time-invariance and causality, impulse response, characterization of linear time-invariant (LTI) systems, unit sample response, convolution summation, step response of discrete time systems, stability.
Convolution integral, co-relations, signal energy and energy spectral density, signal power and power spectral density, properties of power spectral density.

UNIT-5

Time and frequency domain analysis of systems

Analysis of first order and second order systems, continuous-time (CT) system analysis using LT, system functions of CT systems, poles and zeros, block diagram representations; discrete-time system functions, block diagram representation, illustration of the concepts of system bandwidth and rise time through the analysis of a first order CT low pass filter

Text Book: P. Ramakrishna Rao, 'Signal and Systems' 2008 Ed., Tata McGraw Hill, New Delhi

Reference Books:

Chi-Tsong Chen, 'Signals and Systems', 3rd Ed., Oxford University Press, 2004

V. Oppenheim, A.S. Willsky & S. Hamid Nawab, 'Signals & System', Pearson Education, 2nd Ed., 2003.

EEEC-304 SWITCHING THEORY AND LOGIC DESIGN

UNIT-1

Digital system and binary numbers: Signed binary numbers, binary codes.

Gate-level minimization: The map method up to four variable, don't care conditions, POS simplification, NAND and NOR implementation, Quine Mc-Clusky method (Tabular method)

UNIT-2

Combinational Logic: Combinational circuits, analysis procedure, design procedure, binary adder-subtractor, decimal adder, binary multiplier, magnitude comparator, decoders, encoders, multiplexers

UNIT-3

Synchronous Sequential logic: Sequential circuits, storage elements: latches, flip flops, analysis of clocked sequential circuits, state reduction and assignments, design procedure.

Asynchronous Sequential logic: Analysis procedure, circuit with latches, design procedure, reduction of state and flow table, race free state assignment, hazards.

UNIT-4

Registers and counters: Shift registers, ripple counter, synchronous Counter, other counters.

Memory and programmable logic: RAM, ROM, PLA, PAL.

Introduction to RTL.

Text Book: M. Morris Mano and M. D. Ciletti, "Digital Design", 5th Edition, Pearson Education.

Reference Books: 1. Hill & Peterson, "Switching Circuit & Logic Design", Wiley.

2. Mohammad A. Karim and Xinghao Chen, "Digital Design-Basic concepts and Principles", CRC Press Taylor & Francis group, 2010

EHU-301 : Industrial Sociology

Unit-I

Industrial Sociology :Nature and Scope of Industrial Sociology- Development of Industrial Sociology.**Work Stress & its Mangement**

Unit-II

Rise and Development of Industry :Early Industrialism – Types of Productive Systems – The Manorial or Feudal system – The guild system – The domestic or putting-out system – and the factory system – Characteristics of the factory system – causes and Consequences of industrialization.

Unit-III

Industrialization in India. Industrial Poling Resolutions – 1956.

Unit-IV

Contemporary Issues :Grievances and Grievance handling Procedure.

Industrial Disputes : courses, strikes & lockouts, Industrial Relations Machinery Bi-partite & Tri-partite Agreement, Labour courts & Industrial Tribunals, Code of Discipline, Standing order.

References :

1. GISBERT PASCAL, Fundamentals of Industrial sociology, Tata McGraw Hill Publishing Co., New Delhi, 1972.
2. SCHNEIDER ENGNO V., Industrial Sociology 2nd Edition, McGraw Hill Publishing Co., New Delhi, 1979.
3. Mamoria C.B. and Mamoria S., Dynamics of industrial relations in India.
4. Sinha G.P. and P.R.N. Sinha, Industrial Relations and Labour Legislations, New Delhi, Oxford and IBH Publishing Co., 1977.

AUC 001: Cyber security

UNIT-1

Introduction to information systems, Types of information Systems, Development of Information Systems, Introduction to information security, Need for Information security, Threats to Information Systems, Information Assurance, Cyber Security, and Security Risk Analysis.

UNIT-2

Application security (Database, E-mail and Internet), Data Security Considerations-Backups, Archival Storage and Disposal of Data, Security Technology-Firewall and VPNs, Intrusion Detection, Access Control.

Security Threats -Viruses, Worms, Trojan Horse, Bombs, Trapdoors, Spoofs, E-mail viruses, Macro viruses, Malicious Software, Network and Denial of Services Attack, Security Threats to E-Commerce- Electronic Payment System, e-Cash, Credit/Debit Cards. Digital Signature, public Key Cryptography.

UNIT-3

Developing Secure Information Systems, Application Development Security, Information Security Governance & Risk Management, Security Architecture & Design Security Issues in Hardware, Data Storage & Downloadable Devices, Physical Security of IT Assets, Access Control, CCTV and intrusion Detection Systems, Backup Security Measures.

UNIT-4

Security Policies, Why Policies should be developed, WWW policies, Email Security policies, Policy Review Process-Corporate policies-Sample Security Policies, Publishing and Notification Requirement of the Policies.

Information Security Standards-ISO, IT Act, Copyright Act, Patent Law, IPR. Cyber Laws in India; IT Act 2000 Provisions, Intellectual Property Law: Copy Right Law, Software License, Semiconductor Law and Patent Law.

References :

1. Charles P. Pfleeger, Shari Lawerance Pfleeger, "Analysing Computer Security", Pearson Education India.
2. V.K. Pachghare, "Cryptography and information Security", PHI Learning Private Limited, Delhi India.
3. Dr. Surya Prakash Tripathi, Ritendra Goyal, Praveen kumar Shukla, "Introduction to Information Security and Cyber Law" Willey Dreamtech Press.
4. Schou, Shoemaker, "Information Assurance for the Enterprise", Tata McGraw Hill.
5. CHANDER, HARISH, "Cyber Laws And It Protection", PHI Learning Private Limited, Delhi, India

EEC- 351 NETWORK ANALYSIS & SYNTHESIS LAB

1. Study and verification of network theorems with input signal of 1 kHz, 10kHz and 100kHz.
2. Verification of two port network parameters
3. Step and Ramp response of series and parallel RC circuits
4. Verification of properties of RC circuits
5. Verification of properties of RL circuits
6. Verification of properties of LC circuits
7. Verification of inverting, non-inverting and voltage follower VCVS circuits using 741 op-amp
8. Verification of inverting integrator using 741 op-amp
9. Design a finite gain differential amplifier with infinite input impedance and verify the output response.

EEC- 352 ELECTRONIC WORKSHOP & PCB LAB

Objective: To create interest in Hardware Technology.

1. Study of CRO, DMM & Function Generator
2. Identification of Active & Passive Components
3. Winding shop: Step down transformer winding of less than 5VA.
4. Soldering shop: Fabrication of DC regulated power supply
5. PCB Lab: (a) Artwork & printing of a simple PCB. (b) Etching & drilling of PCB.
6. Wiring & fitting shop: Fitting of power supply along with a meter in cabinet.
7. Testing of regulated power supply fabricated.

EEC- 353 LOGIC DESIGN LAB

Objective: To understand the digital logic and create various systems by using these logics.

1. Introduction to digital electronics lab- nomenclature of digital ICs, specifications, study of the data sheet,
Concept of V_{cc} and ground, verification of the truth tables of logic gates using TTL ICs.
2. Implementation of the given Boolean function using logic gates in both SOP and POS forms.
3. Verification of state tables of RS, JK, T and D flip-flops using NAND & NOR gates.
4. Implementation and verification of Decoder/De-multiplexer and Encoder using logic gates.
5. Implementation of 4x1 multiplexer using logic gates.
6. Implementation of 4-bit parallel adder using 7483 IC.
7. Design, and verify the 4-bit synchronous counter.
8. Design, and verify the 4-bit asynchronous counter.
9. Mini Project (Imp)

EEC- 354 ELECTRONIC DEVICES LAB.

Objective: To attain expertise in lab equipment handling and understanding the basic devices, their properties, Characteristics in detail. Along with their practical usage in the circuit

1. **Study of lab equipments and components:** CRO, Multimeter, Function Generator, Power supply- Active, Passive Components & Bread Board.
2. **P-N Junction Diode:** Characteristics of PN Junction diode-Static and dynamic resistance measurement from graph.
3. **Applications of PN junction diode:** Half & Full wave rectifier- Measurement of V_{rms} , V_{dc} , and ripple factor-use of filter- ripple reduction (RC Filter)-Clipper & Clamper
4. **Properties of junctions** Zener diode characteristics. Heavy doping alters the reverse characteristics. Graphical measurement of forward and reverse resistance.
5. **Application of Zener diode:** Zener diode as voltage regulator. Measurement of percentage regulation by varying load resistor.
6. **Characteristic of BJT:** BJT in CB and CE configuration- Graphical measurement of h parameters from input and output characteristics. Measurement of A_v , A_i , R_o and R_i of CE amplifier with potential divider biasing.
7. **Characteristic of FET:** FET in common source configuration. Graphical measurement of its parameters g_m , r_d & m from input and output characteristics.
8. **Characteristic** of silicon-controlled rectifier.
9. **To plot** V-I Characteristics of DIAC.
10. **To draw** V-I characteristics of TRIAC for different values of Gate Currents.

EEC-401DATA STRUCTURE

UNIT-1

Introduction: Basic Terminology, Elementary Data Organization, Algorithm, Efficiency of an Algorithm, Time and Space Complexity, Asymptotic notations: Big-Oh, Time-Space trade-off. Abstract Data Types (ADT) Arrays: Definition, Single and Multidimensional Arrays, Representation of Arrays: Row Major Order, and Column Major Order, Application of arrays, Sparse Matrices and their representations. Linked lists: Array Implementation and Dynamic Implementation of Singly Linked Lists, Doubly Linked List, Circularly Linked List, Operations on a Linked List. Insertion, Deletion, Traversal, Polynomial Representation and Addition, Generalized Linked List

UNIT-2

Stacks: Abstract Data Type, Primitive Stack operations: Push & Pop, Array and Linked Implementation of Stack in C, Application of stack: Prefix and Postfix Expressions, Evaluation of postfix expression, Recursion, Tower of Hanoi Problem, Simulating Recursion, Principles of recursion, Tail recursion, Removal of recursion
Queues, Operations on Queue: Create, Add, Delete, Full and Empty, Circular queues, Array and linked implementation of queues in C, Dequeue and Priority Queue.

UNIT-3

Trees: Basic terminology, Binary Trees, Binary Tree Representation: Array Representation and Dynamic Representation, Complete Binary Tree, Algebraic Expressions, Extended Binary Trees, Array and Linked Representation of Binary trees, Tree Traversal algorithms: In order, Preorder and Post order, Threaded Binary trees, Traversing Threaded Binary trees, Huffman algorithm.

UNIT-4

Graphs: Terminology, Sequential and linked Representations of Graphs: Adjacency Matrices, Adjacency List, Adjacency Multi list, Graph Traversal : Depth First Search and Breadth First Search, Connected Component, Spanning Trees, Minimum Cost Spanning Trees: Prims and Kruskal algorithm. Transitive Closure and Shortest Path algorithm: Warshal Algorithm and Dijkstra Algorithm, Introduction to Activity Networks.

UNIT-4

Searching : Sequential search, Binary Search, Comparison and Analysis

Internal Sorting: Insertion Sort, Selection, Bubble Sort, Quick Sort, Two Way Merge Sort, Heap Sort, Radix Sort

Text book:

1. Aaron M. Tenenbaum, Yedidiah Langsam and Moshe J. Augenstein “Data Structures Using C and C++” , PHI

References

1. Horowitz and Sahani, “Fundamentals of Data Structures”, Galgotia Publication
2. Jean Paul Trembley and Paul G. Sorenson, “An Introduction to Data Structures with applications”, McGraw Hill
3. R. Kruse etal, “Data Structures and Program Design in C”, Pearson Education
4. Lipschutz, “Data Structures” Schaum’s Outline Series, TMH
5. G A V Pai, “Data Structures and Algorithms”, TMH

EEC-402 ELECTRONIC CIRCUITS

UNIT-1

Operational Amplifier: Inverting and non-inverting configurations, difference amplifier, Effect of finite open loop gain and bandwidth on circuit performance, Large signal operation of op-amp.

UNIT-2

MOSFET: Review of device structure operation and V-I characteristics. Circuits at DC, MOSFET as Amplifier and switch, Biasing in MOS amplifier circuits, small-signal operation and models, single stage MOS amplifier, MOSFET internal capacitances and high frequency model, frequency response of CS amplifier

UNIT-3

BJT: Review of device structure operation and V-I characteristics, BJT circuits at DC, BJT as amplifier and switch, biasing in BJT amplifier circuit, small-signal operation and models, single stage BJT amplifier, BJT internal capacitances and high frequency model, frequency response of CE amplifier.

UNIT-4

Differential Amplifier: MOS differential pair, small signal operation of the MOS differential pair, BJT differential pair, other non-ideal characteristic of the Differential amplifier (DA), DA with active load.

UNIT-5

Feedback: The general feedback structure, properties of negative feedback, the four basic feedback topologies, the series-shunt feedback amplifier, the series-series feedback amplifier, the shunt-shunt and shunt-series feedback amplifier.

Oscillators: Basic principles of sinusoidal oscillators, op-amp RC oscillator circuits, LC oscillator.

Text Book: A. S. Sedra and K. C. Smith, "Microelectronic Circuits", Oxford University Press, 5th Ed.

Reference Books: Jacob Millman and Arvin Grabel, "Microelectronics", 2nd Ed TMH.

EEC-403 ELECTRONIC MEASUREMENTS AND INSTRUMENTATION

UNIT-1

Unit, dimensions and standards: Scientific notations and metric prefixes.

SI electrical units, SI temperature scales, Other unit systems, dimension and standards.

Measurements and Measurement System: Significance of Measurements, Classification of Instruments. Measurement Errors: Gross error, systematic error, absolute error and relative error, accuracy, precision, resolution and significant figures, Measurement error combination, basics of statistical analysis. PMMC instrument, galvanometer, DC ammeter, DC voltmeter, series ohm meter.

UNIT-2

Transistor voltmeter circuits, AC electronic voltmeter, current measurement with electronic instruments, probes Digital voltmeter systems, digital multimeters, digital frequency meter system, Electro dynamic Instrument.

UNIT-3

Voltmeter and ammeter methods, Wheatstone bridge, low resistance measurements, low resistance measuring instruments. AC bridge theory, capacitance bridges, Inductance bridges, Q meter

UNIT-4

CRO: CRT, wave form display, time base, dual trace oscilloscope,

Measurement of voltage, frequency and phase by CRO, Oscilloscope probes, Oscilloscope specifications and performance.

Delay time based Oscilloscopes, Sampling Oscilloscope, DSO, DSO applications.

UNIT-5

Instrument calibration: Comparison method, digital multimeters as standard instrument, calibration instrument

Recorders: X-Y recorders, plotters.

Text Book:

1. David A. Bell, "Electronic Instrumentation and Measurements", 3rd Ed., Oxford University press, 2011.

2. A. K. Sawhney, "Electrical and Electronic Measurements and Instrumentation" 19th Ed., Dhanpat Rai, 2011.

Reference Books:

1. Oliver and Cage, "Electronic Measurements and Instrumentation", TMH, 2009.

2. Alan S. Morris, "Measurement and Instrumentation Principles", Elsevier (Buterworth Heinmann), 2008.

EEC-404 ELECTROMAGNETIC FIELD THEORY

UNIT-1

Coordinate systems and transformation: Cartesian coordinates, circular cylindrical coordinates, spherical coordinates Vector calculus: Differential length, area and volume, line surface and volume integrals, del operator, gradient of a scalar, divergence of a vector and divergence theorem, curl of a vector and Stoke's theorem, Laplacian of a scalar.

UNIT-2

Electrostatics: Electrostatic fields, Coulombs law and field intensity, Electric field due to charge distribution, Electric flux density, Gauss's Law – Maxwell's equation, Electric dipole and flux lines, energy density in electrostatic fields.

Electric field in material space: Properties of materials, convection and conduction currents, conductors, polarization in dielectrics, dielectric constants, continuity equation and relaxation time, boundary condition.

Electrostatic boundary value problems: Poisson's and Laplace's equations, general procedures for solving Poisson's or Laplace's equations, resistance and capacitance, method of images.

UNIT-3

Magnetostatics: Magneto-static fields, Biot-Savart's Law, Ampere's circuit law, Maxwell's equation, application of ampere's law, magnetic flux density- Maxwell's equation, Maxwell's equation for static fields, magnetic scalar and vector potential.

Magnetic forces, materials and devices: Forces due to magnetic field, magnetic torque and moment, a magnetic dipole, magnetization in materials, magnetic boundary conditions, inductors and inductances, magnetic energy

UNIT-4

Waves and applications: Maxwell's equation, Faraday's Law, transformer and motional electromotive forces, displacement current, Maxwell's equation in final form.

Electromagnetic wave propagation: Wave propagation in lossy dielectrics, plane waves in lossless dielectrics, plane wave in free space, plane waves in good conductors, power and the pointing vector, reflection of a plane wave in a normal incidence.

UNIT-5

Transmission lines: Transmission line parameters, Transmission line equations, input impedance, standing wave ratio and power, The Smith chart, Some applications of transmission lines.

Text Book: M. N. O. Sadiku, "Elements of Electromagnetics", 4th Ed, Oxford University Press.

Reference Books: W. H. Hayt and J. A. Buck, "Electromagnetic field theory", 7th Ed., TMH.
Pramanik-Electromagnetism: Vol.1-Theory, PHI Learning Pvt. Ltd.

EHU-401: Industrial Psychology

Unit-I

Introduction to Industrial Psychology – Definitions & Scope

Major influences on industrial Psychology- Scientific management and human relations schools
Hawthorne Experiments

Unit-II

Individual in Workplace Motivation and Job satisfaction , stress management. Organizational culture, Leadership & group dynamics.

Unit-III

Work Environment & Engineering Psychology-fatigue. Boredom, accidents and safety. Job Analysis, Recruitment and Selection – Reliability & Validity of recruitment tests.

Unit –IV

Performance Management : Training & Development.

References :

1. Miner J.B. (1992) Industrial/Organizational Psychology. N Y : McGraw Hill.
2. Blum & Naylor (1982) Industrial Psychology. Its Theoretical & Social Foundations CBS Publication.
3. Aamodt, M.G. (2007) Industrial/Organizational Psychology : An Applied Approach (5th edition) Wadsworth/Thompson : Belmont, C.A.
4. Aswathappa K. (2008). Human Resource Management (fifth edition) New Delhi : Tata McGraw Hill

EEC- 451 DATA STRUCTURE LAB

Program in C or C++ for following:

1. To implement addition and multiplication of two 2D arrays.
2. To transpose a 2D array.
3. To implement stack using array.
4. To implement queue using array.
5. To implement circular queue using array.
6. To implement stack using linked list.
7. To implement queue using linked list.
8. To implement circular queue using linked list.
9. To implement binary tree using linked list.
10. To implement binary search tree using linked list.
11. To implement tree traversals using linked list.
12. To implement BFS using linked list.
13. To implement DFS using linked list.
14. To implement Linear Search.
15. To implement Binary Search.
16. To implement Bubble Sorting.
17. To implement Selection Sorting.
18. To implement Insertion Sorting.
19. To implement Merge Sorting.
20. To implement Heap Sorting.

EEC- 452 ELECTRONIC CIRCUITS LAB

Objective - To design and implement the circuits to gain knowledge on performance of the circuits and its applications.

- 1. Measurement of Operational Amplifier Parameters**-Common Mode Gain, Differential Mode Gain, CMRR, Slew Rate.
- 2. Applications of Op-amp**- Op-amp as summing amplifier, Difference amplifier, Integrator and differentiator
- 3. Field Effect Transistors**-Single stage Common source FET amplifier –plot of gain in dB Vs frequency, Measurement of, bandwidth, input impedance, maximum signal handling capacity (MSHC) of an amplifier
- 4. Bipolar Transistors**- Design of single stage RC coupled amplifier –design of DC biasing circuit using potential divider arrangement –Plot of frequency versus gain in dB. Measurement of bandwidth of an amplifier, input impedance and Maximum Signal Handling Capacity of an amplifier.
- 5. Two stage Amplifier**. Plot of frequency Vs gain. Estimation of Q factor, bandwidth of an amplifier
- 6. Common Collector Configuration-Emitter Follower** (using Darlington pair)-Gain and input impedance
- 7. Power Amplifiers**-Push pull amplifier in class B mode of operation –measurement of gain.
- 8. Differential Amplifier** –Implementation of transistor differential amplifier .Non ideal characteristics of differential amplifier
- 9. Oscillators** -Sinusoidal Oscillators- (a) Wein bridge oscillator (b) phase shift oscillator
- 10. Simulation of Amplifier** circuits studied in the lab using any available simulation software and measurement of bandwidth and other parameters with the help of simulation software.

EEC- 453 SIGNAL AND SYSTEMS LAB

1. To plot basic functions using MATLAB.
2. To study the basic operations on signals using MATLAB.
3. Generation of even and odd parts of a signal by using MATLAB.
4. To study linearity of the system and verify whether the given system is linear by using MATLAB.
5. To study time invariance property of system and verify whether the given system is time invariant, using MATLAB.
6. To plot the magnitude and phase spectra of the periodic signal $x(t)$ with a period equal to 8 and defined as $x(t)=\lambda(t)$ for $|t|\leq 4$.
7. By using MATLAB compute the response of an LTI system with system function $H(s)$ to an input $x(t)$. Suppose $H(s) = 5s/s^2 + 2s + 101$, find the response to the input $x(t) = \cos(2\pi t) u(t)$.
8. To study z-transform using MATLAB
9. To study Laplace transform by using MATLAB.
10. To study Fourier transform by using MATLAB.
11. To study the transfer function and plot the frequency response of a system using MATLAB.
12. To find impulse response $h[n]$ of a system from its transfer function or difference equations relating input and output using MATLAB.
13. To find convolution of two sequences by using MATLAB.
14. To find power and power spectral density of the signal by using MATLAB.
15. To study DTFT and inverse DTFT by using MATLAB

EEC- 454 ELECTRONIC MEASUREMENT LAB

1. Study of semiconductor diode voltmeter and its use as DC average responding AC voltmeter .
2. Study of L.C.R. bridge and determination of the value of the given components.
3. Study of distortion factor meter and determination of the % distortion of the given oscillator.
4. Study of the transistor tester and determination of the parameters of the given transistors.
5. Study of the following transducer (i) PT-100 trans (ii) J- type trans. (iii) K-type trans (iv) Presser trans
6. Measurement of phase difference and frequency using CRO (lissajous figure)
7. Measurement of low resistance Kelvin's double bridge.
8. Radio Receiver Measurements